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UNITED STATES PATENT AND TRADEMARK OFFICE
PATENT TRIAL AND APPEAL BOARD

Ex parte SAMSUNG DISPLAY CO., LTD.
(Application 11/689,390)

Appeal 2011-011830
from Technology Center 1700
Galen Hauth, Examiner

Before RICHARD TORCZON, DONNA M. PRAISS and
CHRISTOPHER L. CRUMBLEY, *Administrative Patent Judges*.

TORCZON, *Administrative Patent Judge*.

DECISION ON APPEAL

The appellant (Samsung) seeks relief from the final rejection of claims 1-5, 8-14 and 19-21.¹ We AFFIRM.

OPINION

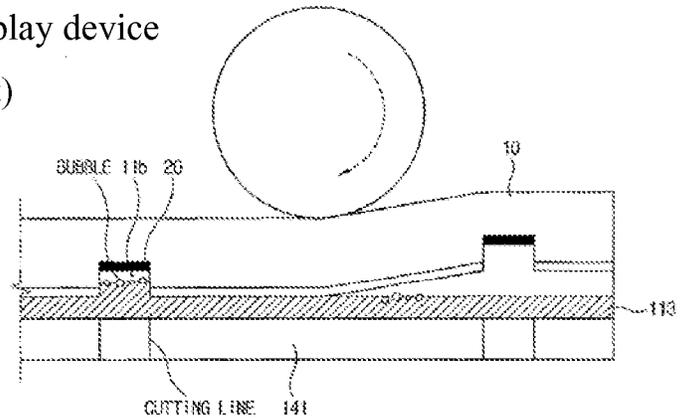
BACKGROUND

Samsung's specification "relates to a mold for manufacturing a display

¹ Claim 22 appears in the claims appendix of Samsung's appeal brief, but is not mentioned in the final rejection or in Samsung's request for relief. Accordingly, we do not consider claim 22 to be properly before us.

device and a method of manufacturing a display device using the same."² Samsung Figure 5B (right)

illustrates a step in an embodiment of a manufacturing method of a liquid crystal display device.³ A roller (unnumbered circle rotating clockwise) presses a mold (with a mold main body **10** and a



groove with a subgroove **11b**) onto an organic layer **113** on a substrate **141** such that bubbles get trapped in the subgroove **11b**.⁴

Claim 1, the only independent claim, defines the invention as:⁵

A method of manufacturing a display device, comprising:

providing a mother insulating substrate which comprises usage regions arranged in a matrix shape, and a removal region formed between the usage regions;

forming an organic layer on the mother insulating substrate;

arranging a flexible mold formed with a pattern part corresponding to the usage regions and *a plurality of grooves corresponding to at least a part of the removal region and formed in a grid shape*, on the organic layer;

moving a pressure means in a direction parallel to a top surface of the mold to sequentially press the mold to the organic layer;

separating the mold from the organic layer, and

forming a plurality of thin film transistor substrates by cutting the mother insulating substrate along a cutting line,

wherein the mold comprises a mold main body which comprises a light transmissive polymer and a light blocking layer

² Spec. ¶002.

³ *Id.* ¶0026.

⁴ *Id.* ¶¶0052-0067.

⁵ Claim language comes for the claims appendix of the brief. Br. 10. Emphasis has been added for the limitation that Samsung particularly contests. *Id.* 6.

which is formed on the mold main body to correspond to the removal region,

wherein the light blocking layer is formed on a lower part of the groove,

wherein bubbles formed between the mold and the organic layer move toward a direction of moving the pressure means, and

wherein the moved bubbles are trapped in the groove, and

wherein the removal region is formed according to the cutting line.

The examiner rejected claims 1-5, 9-12 and 19-21 as having been obvious,⁶ relying on the combined disclosures of Maekawa,⁷ Choi,⁸ Guo⁹ and Tan.¹⁰ The examiner rejected the remaining claims as having been obvious, using the base combination plus one additional reference for claim 8¹¹ and a different additional reference for claims 13-14.¹² Samsung relies on the limitations of claim 1 to urge prejudicial error in the rejection of all of the claims;¹³ hence, we have no basis for reversing the other rejections apart from the reasons given for claim 1.¹⁴

⁶ Final Rej. 2, citing 35 U.S.C. 103.

⁷ S. Maekawa, *Method for manufacturing semiconductor device*, US 2005/0266693 A1.

⁸ B.J. Choi et al., *Method and system of automatic fluid dispensing for imprint lithography processes*, US 2002/0094496 A1.

⁹ L.J. Guo and X. Cheng, *Combined nanoimprinting and photolithography for micro and nano devices fabrication*, WO 2005/029179 A2.

¹⁰ H. Tan, A. Gilbertson & S.Y. Chou, *Roller Nanoimprint Lithography*, 16(6) J. VACUUM SCI. TECH. B 3926 (1998).

¹¹ Final Rej. 8, citing D.P. Mancini, D.J. Resnick & W.J. Dauksher, *Lithographic template and method of formation and use*, US 2002/0122995 A1.

¹² Final Rej. 9, citing Y. Yamanaka et al., *Reflector, method of fabricating the same, reflective display device comprising reflector, and method of fabricating the same*, US 6,452,653 B1 (2002).

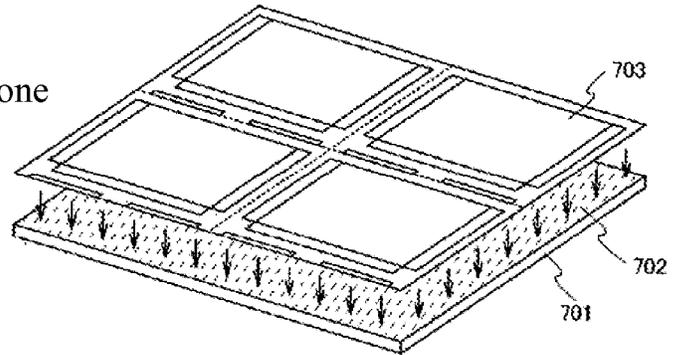
¹³ Br. 6-9.

¹⁴ *Hyatt v. Dudas*, 551 F.3d 1307, 1313 (Fed. Cir. 2008).

FACTS AND FINDINGS

- [1] Maekawa discloses a method for manufacturing a semiconductor device applying a nanoimprinting method.¹⁵
- [2] Nanoimprinting is a method for reducing the costs of lithography.¹⁶
- [3] Maekawa's invention may be used to manufacture semiconductor display devices.¹⁷
- [4] Maekawa's invention may be used to manufacture thin-film transistor devices.¹⁸
- [5] Maekawa teaches using a single mold pressed to the resist on a substrate to form multiple devices.¹⁹

- [6] Maekawa Figure 11A (right) depicts one mode of pattern forming on a large substrate using nanoimprinting,²⁰ in which a mold **703** is pressed onto a substrate **701** with resist **702**.²¹



- [7] The examiner reasons that Maekawa's formation of several devices on the same substrate implies subsequent cutting of the substrate in the grid-like, non-device zones to separate the devices.²²
- [8] The examiner found that Maekawa does not teach:²³

¹⁵ Maekawa ¶0002.

¹⁶ *Id.* ¶0004.

¹⁷ *Id.* ¶0013.

¹⁸ *Id.* ¶0014.

¹⁹ *Id.* ¶¶0125-26.

²⁰ *Id.* ¶0025.

²¹ *Id.* ¶0126.

²² Ans. 12.

²³ Final Rej. 3.

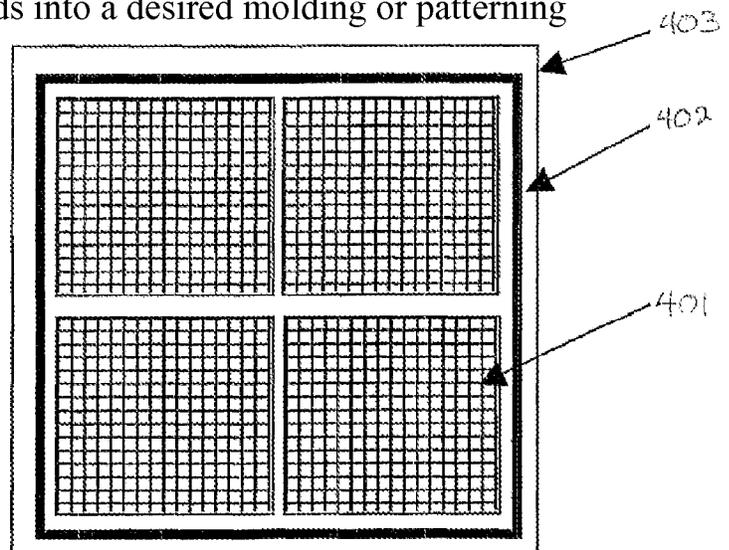
- a. the use of a flexible mold with a moving pressure source parallel to the top,
- b. grooves present in the mold main body corresponding to the non-usage regions,
- c. a light-blocking layer in the groove.

[9] Choi discloses methods of dispensing fluids applicable to imprint lithography processes.²⁴

[10] Bubbles and distortions caused by imprinting pressure were known problems.²⁵

[11] Choi teaches the use of an entrainment channel or kerf in the template for capturing excess fluid before it spreads into a desired molding or patterning area.²⁶

[12] Choi Figure 4 (right) is a bottom view of a patterned template,²⁷ showing a patterning region **401**, an entrainment channel **402** and a template edge **403** for holding the template.²⁸



[13] The examiner reasons that a person having ordinary skill in the art would move Choi's entrainment channel

²⁴ Choi ¶0003.

²⁵ *Id.* ¶0008.

²⁶ *Id.* ¶¶0090 & 0094.

²⁷ *Id.* ¶0026.

²⁸ *Id.* ¶0090.

to the cutting zones implicit in Maekawa because it would be reasonably expected to improve the imprinting process by better controlling the unwanted spread of resist across the substrate.²⁹

[14] The examiner relies on Guo for disclosures that Samsung does not dispute in this appeal.³⁰

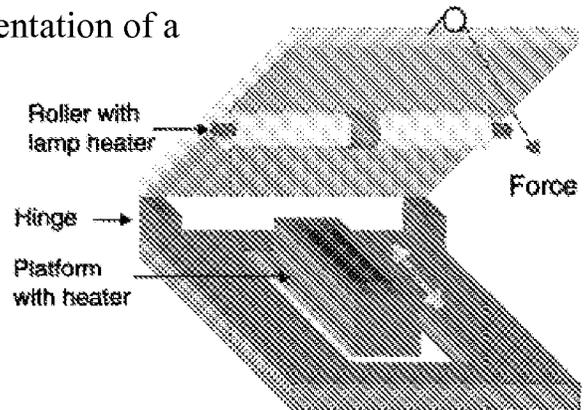
[15] Tan discusses roller nanoimprint lithography as an improvement over conventional nanoimprint lithography.³¹

[16] Tan teaches that the mold may be on the roller itself or the roller may press a flat mold onto the substrate, in which case the mold must be flexible enough to deform under the roller.³²

[17] The roller locally heats the resist on the substrate so the resist flows.³³

[18] Tan Figure 1 (right) is a schematic representation of a roller nanoimprint system with a roller, a movable platform, a hinge and heaters.³⁴

[19] Tan reports that the roller method results in "few air bubbles" and speculates that "the roller pushes the air out."³⁵



ANALYSIS

Samsung contends that the cited references fail to disclose a groove formed in a grid shape disposed in a portion corresponding to the removal section formed along a cutting line. Samsung notes that the examiner conceded as a difference

²⁹ Final Rej. 3.

³⁰ *Id.* 3-4 (discussing Guo) and Br. 8 (discussing only Maekawa and Choi).

³¹ Tan 3926, abstract.

³² *Id.* 3926, right col.

³³ *Id.*

³⁴ *Id.* 3927, left col.

³⁵ *Id.* 3928, left col.

that Maekawa does not teach grooves present in the mold main body corresponding to the non-usage regions. Samsung urges that the examiner erred in his understanding of Choi's groove, which Samsung states (1) is formed along the periphery of the template rather than in the claimed "grid shape" along a "cutting line" and (2) is a pattern forming part, not a part removed by cutting a mother insulating substrate along a cutting line.³⁶

Samsung misapprehends the examiner's findings. The examiner does not rely on Choi to teach grid-like grooves aligned with cutting lines. Rather, the examiner found that Maekawa at least suggests grid-like cutting zones around the patterned devices, while Choi teaches the desirability of entrainment channels outside the patterned zones. The examiner further found that a person having ordinary skill in the art reasonably expected that moving the entrainment channels to the grid-like, non-device regions would improve the ability of the channels to entrain, presumably because each channel would be closer to the problem and have to entrain flow over a smaller area. Once the channel was moved, it would automatically be in the cutting zone.

The examiner's position is reasonable and is consistent with the references on which he relies. Samsung has not demonstrated prejudicial error in the final rejection.

HOLDING

Final rejection of claims 1-5, 8-14 and 19-21 is—

AFFIRMED

bar

For the appellant: MARK A. PELLEGRINI,³⁷ Innovation Counsel LLP, of Cupertino, California.

³⁶ Br. 7-8.

³⁷ 37 C.F.R. § 1.34 (Acting in a representative capacity).